

# Curriculum for B. Tech. (Computer Science and Engineering) 3rd Sem to 8th Sem

Subject Name	Subject Code	L-T-P	Credit	Contact Hour
<b>SEMESTER - III</b>				
<a href="#">Introduction to Electronics</a>	<b>EC2L001</b>	3-1-0	4	4
<a href="#">Introduction to Material Science and Engineering</a>	<b>ID2L001</b>	2-0-0	2	2
<a href="#">Introduction to Bioscience and Technology</a>	<b>ID2L002</b>	2-0-0	2	2
<a href="#">Discrete Structures</a>	<b>CS2L001</b>	3-1-0	4	4
<a href="#">Signals and Systems</a>	<b>EC2L002</b>	3-1-0	4	4
Breadth – 1		3-0-0	3	3
<a href="#">Introduction to Electronics Laboratory</a>	<b>EC2P001</b>	0-0-3	2	3
<a href="#">Signals and Systems Laboratory</a>	<b>EC2P002</b>	0-0-3	2	3
Project Seminar	<b>EE2S001</b>	0-0-3	2	0
		<b>Total</b>	<b>25</b>	<b>25</b>
<b>SEMESTER – IV</b>				
Lateral -1			3/4	3/4
Breadth – 2			3/4	3/4
Environmental Science Technology and Management	<b>ID2L003</b>	2-0-0	2	2
Combinatorics, Probability and Statistics ( <i>Mathematics VI</i> )	<b>MA2L006</b>	3-1-0	4	4
<a href="#">Design and Analysis of Algorithms</a>	<b>CS2L002</b>	3-1-0	4	4
<a href="#">Digital Electronics Circuits</a>	<b>EC2L004</b>	3-1-0	4	4
<a href="#">Design and Analysis of Algorithms Laboratory</a>	<b>CS2P001</b>	0-0-3	2	3
<a href="#">Digital Electronics Circuits Laboratory</a>	<b>EC2P004</b>	0-0-3	2	3
		<b>Total</b>	<b>24/26</b>	<b>26/28</b>
<b>SEMESTER – V</b>				
Lateral -2			3/4	3/4
Breadth-3			3	3

<a href="#">Formal Languages and Automata Theory</a>	<b>CS3L001</b>	3-1-0	4	4
<a href="#">Computer Organization and Architecture</a>	<b>CS3L002</b>	4-0-0	4	4
<a href="#">Database Systems</a>	<b>CS3L003</b>	3-1-0	4	4
<a href="#">Computer Organization and Architecture Laboratory</a>	<b>CS3P001</b>	0-0-3	2	3
<a href="#">Database Systems Laboratory</a>	<b>CS3P002</b>	0-0-3	2	3
		<b>Total</b>	<b>22/23</b>	<b>24/25</b>
<b>SEMESTER - VI</b>				
Lateral -3			3/4	3/4
<a href="#">Operating Systems</a>	<b>CS3L005</b>	4-0-0	4	4
<a href="#">Computer Networks</a>	<b>CS3L006</b>	3-1-0	4	4
Elective-1		3/4-0-0/3	3/4/5/6	3/4/6/7
<a href="#">Operating Systems Laboratory</a>	<b>CS3P003</b>	0-0-3	2	3
<a href="#">Computer Networks Laboratory</a>	<b>CS3P004</b>	0-0-3	2	3
		<b>Total</b>	<b>18/20/21/22</b>	<b>20/21/23/24/25</b>
<b>SEMESTER - VII</b>				
<a href="#">Compiler Design</a>	<b>CS4L001</b>	3-1-0	4	4
Elective-2		3-0/1--0/3	3/4/5/6	3/4/6/7
Elective-3		3-0-0	4/5	4/5
Elective-4		3-0-0	3	3
<a href="#">Compiler Design Laboratory</a>	<b>CS4P001</b>	0-0-3	2	3
Industrial Training Defense	<b>CS4T001</b>	0-0-0	2	0
Project- Part 1	<b>CS4D001</b>	0-0-0	4	0
		<b>Total</b>	<b>22/23/24/25/26</b>	<b>21/22/23/24/25/26</b>
<b>SEMESTER - VIII</b>				
Breadth-4			3/4	3/4
Elective-5		3-0-0	3/4	3/4
Elective-6		3-0-0	3	3
Project-Part 2	<b>CS4D002</b>	0-0-0	6	0
		<b>Total</b>	<b>15/16/17</b>	<b>9/10/11</b>
		<b>G. Total</b>		
		<i>*Excluding EAA-1 &amp; 2</i>		